

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	2332	(crossbar or crossconnect).ti. and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:13
L3	16	(crossbar or crossconnect).ti. and ((input or ingress) same (output or egress) same port) and (memory or storage) and (queue or fifo or buffer) and (multicast) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:06
L4	12	(crossbar or crossconnect).ti. and ((input or ingress) same (output or egress) same port) and (memory or storage) and (queue or fifo or buffer) and (broadcast) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:06
L5	5	(crossbar or crossconnect).ti. and ((input or ingress) same (output or egress) same port) and (memory or storage) and (queue or fifo or buffer) and (broadcast) and (@rlad<"20010111" or @ad<"20010111") not L3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:11
L8	20463474	((crossbar or crossconnect) and (multicast or broadcast)).ti. (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:12
L9	8	((crossbar or crossconnect) and (multicast or broadcast)).ti. and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:13
S1	291	(multicast and crossbar and header) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 11:29

EAST Search History

S2	27	(multicast).ti. and (crossbar and header) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 11:29
S3	29	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 11:32
S4	26	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not (routing adj table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 08:55
S5	26	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not ((routing or (look adj table)) adj table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 11:39
S6	22	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not ((routing or (look adj up)) adj table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 12:16
S7	22	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not ((routing or (look adj up)) adj table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 12:18
S8	22	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not ((routing or (look adj up)) adj table\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/07 12:20
S9	19	(multicast).ti. and ((crossbar or crosspoint) and header) and (@rlad<"20010111" or @ad<"20010111") not ((routing or (lookup)) adj table\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 08:11

EAST Search History

S12	2	(crosspoint or crossbar) and (scheduler same (memory or buffer or queue or fifo) same overflow) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 08:43
S13	1	"3835260".pn. and (overflow same bus) and (@rlad<"20010111" or @ad<"20010111")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/08 08:43

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	21	((crossconnect\$6 or crossbar) and (input or ingress) and (output or egress) and port and (memory or storage) and (multicast\$5 or broadcast\$5)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:30
L11	155	((crossconnect\$6 or crossbar) and (input or ingress) and (output or egress) and port and (memory or storage)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/11 09:48

Scholar

Results 1 - 5 of 5 for **port input output memory multicast crossbar OR crosspoint "switch"**. (0.08 seconds)

Did you mean: port input output memory multicast crossbar OR crosspoint "**switch**"

Architectural choices in large scale ATM switches - group of 17 »

J Turner, N Yamanaka - IEICE Trans. Commun, 1998 - cs.berkeley.edu

... a separate linked-list queue for each **output port**, or each ... Another benefit of **input** buffering is that it does ... relative to a system using **output** queuing or ...

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Autonet: a high-speed, self-configuring local area network using point-to-point links - group of 16 »

MD Schroeder, AD Birrell, M Burrows, H Murray, RM ... - Selected Areas in Communications, IEEE Journal on, 1991 - ieeexplore.ieee.org

... Backpressure through the network can make **output** progress on one **port** dependent upon **input** progress on another **port** of the same switch. ...

Cited by 322 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

A high performance transparent bridge - group of 2 »

M Zitterbart, AN Tantawy, DN Serpanos - IEEE/ACM Transactions on Networking (TON), 1994 - portal.acm.org

... accesses to determine the target outbound **port(s)** of ... transmission, the frame flows through the **output** elasticity buffer ... only once in the data **memory** and moved ...

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A High Performance Transparent Bridge

I INTRODUCTION - IEEE/ACM TRANSACTIONS ON NETWORKING, 1994 - ieeexplore.ieee.org

... accesses to determine the target outbound **port(s)** of ... transmission, the frame flows through the **output** elasticity buffer ... only once in the data **memory** and moved ...

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Book Network-Based Parallel Computing

B Falsafi, M Lauria - 2000 - Springer

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Did you mean to search for: port input output memory multicast crossbar OR crosspoint "**switch**"

port input output memory multicast Search

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar

Results 1 - 6 of 6 for **port input output memory broadcast switch crossbar OR crosspoint**. (0.22 seconds)

Did you mean: **port input output memory broadcast *switch* crossbar OR crosspoint**

Optical Interconnects

JA Neff - Signals, Systems and Computers, 1988. Twenty-Second Asilomar ..., 1988 - [ieeexplore.ieee.org](#)
... registers, an arithmetic & logic unit, and some cache **memory**. ... n2 individual channels;
ie, each connection between an **input port** and an **output port** has a ...

[Web Search](#)

Architectural choices in large scale ATM switches - group of 17 »

J Turner, N Yamanaka - IEICE Trans. Commun, 1998 - [cs.berkeley.edu](#)
... a separate linked-list queue for each **output port**, or each ... Another benefit of **input**
buffering is that it does ... relative to a system using **output** queuing or ...
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Autonet: a high-speed, self-configuring local area network using point-to-point links - group of 16 »

MD Schroeder, AD Birrell, M Burrows, H Murray, RM ... - Selected Areas in Communications, IEEE Journal on, 1991 - [ieeexplore.ieee.org](#)
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upon **input** progress on another **port** of the same switch. ...
Cited by 322 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

A high performance transparent bridge - group of 2 »

M Zitterbart, AN Tantawy, DN Serpanos - IEEE/ACM Transactions on Networking (TON), 1994 - [portal.acm.org](#)
... transmission, the frame flows through the **output** elasticity buffer of ... mB address access
data FDB logic **memory** ... as the associated MAC address and **port** identifier. ...
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A High Performance Transparent Bridge

I INTRODUCTION - IEEE/ACM TRANSACTIONS ON NETWORKING, 1994 - [ieeexplore.ieee.org](#)
... the frame flows through the **output** elasticity buffer ... **Memory** modules are built mostly
using static RAMs with 30 ... as the associated MAC address and **port** identifier ...
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Book Network-Based Parallel Computing

B Falsafi, M Lauria - 2000 - Springer
[Related Articles](#) - [Web Search](#) - [Library Search](#)

Did you mean to search for: **port input output memory broadcast *switch* crossbar OR crosspoint**

port input output memory broadcast Search

Scholar

Results 1 - 10 of 10 for **port input output memory switch crossbar OR crosspoint**. (0.13 seconds)

Did you mean: port input output memory **switch** crossbar OR crosspoint

Emulation of an **output** queued switch with a combined **input** outputqueued switch

TH Lee, YW Kuo, JC Huang - ATM Workshop, 1999. IEEE Proceedings, 1999 - [ieeexplore.ieee.org](#)
... problem because the advancement in **memory** bandwidth is ... to improve the performance of an **input** queued (IQ ... Because of speedup, an **output** port may receive cells ...
Cited by 7 - [Related Articles](#) - [Web Search](#)

Optical Interconnects

JA Neff - Signals, Systems and Computers, 1988. Twenty-Second Asilomar ..., 1988 - [ieeexplore.ieee.org](#)
... arithmetic & logic unit, and some cache **memory**. ... to handle 660 Mbits/second on each **port**. ... electronic **crossbar** chip, while the **input/output** interconnections are ...
[Web Search](#)

Design and implementation of a routing switch for irregular interconnection networks

HC Chi, CM Wu - VLSI Technology, Systems, and Applications, 2001. ..., 2001 - [ieeexplore.ieee.org](#)
... tens or hundreds of processors and **memory** modules for ... It assigns the **output** ports to the packets at the ... When an **input** port organization unit has enough buffer ...
[Web Search](#) - [BL Direct](#)

Architectural choices in large scale ATM switches - group of 17 »

J Turner, N Yamanaka - IEICE Trans. Commun, 1998 - [cs.berkeley.edu](#)
... a separate linked-list queue for each **output** port, or each ... Another benefit of **input** buffering is that it does ... relative to a system using **output** queuing or ...
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Distributed Routing Algorithm in Clos Network of Variable Bit Rate TDM Switch - group of 2 »

H Morino, H Aida, T Saito - Proceedings of the IFIP TC6 WG6. 7 Fifth International ..., 1999 - [ocn.ne.jp](#)
... 2 **Input** Port ... Timeslot position number **Output** Port 32 ... [2] Patel J., "Performance of processor -**memory** interconnections for multiprocessors", IEEE Trans. ...
[Related Articles](#) - [View as HTML](#) - [Web Search](#)

Autonet: a high-speed, self-configuring local area network using point-to-point links - group of 16 »

MD Schroeder, AD Birrell, M Burrows, H Murray, RM ... - Selected Areas in Communications, IEEE Journal on, 1991 - [ieeexplore.ieee.org](#)
... Backpressure through the network can make **output** progress on one **port** dependent upon **input** progress on another **port** of the same switch. ...
Cited by 322 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

A Rearrangeable Multichannel Free-Space Optical

K Noguchi, T Sakano, T Matsumoto - JOURNAL OF LIGHTWAVE TECHNOLOGY, 1991 - [ieeexplore.ieee.org](#)
... these reports have pointed out, however, the **crossbar** network requires ... The optical **input** port is composed of an 8 x 8 ... fiber to colli- mate the fiber **output** beams ...
[Web Search](#)

A high performance transparent bridge - group of 2 »

M Zitterbart, AN Tantawy, DN Serpanos - IEEE/ACM Transactions on Networking (TON), 1994 - [portal.acm.org](#)
... accesses to determine the target outbound **port(s)** of ... transmission, the frame flows through the **output** elasticity buffer ... only once in the data **memory** and moved ...
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A High Performance Transparent Bridge

I INTRODUCTION - IEEE/ACM TRANSACTIONS ON NETWORKING, 1994 - ieeexplore.ieee.org
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book Network-Based Parallel Computing
B Falsafi, M Lauria - 2000 - Springer
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Did you mean to search for: port input output memory **switch** crossbar OR crosspoint

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